

# Doctor of Philosophy - Electrical Engineering

2 Graduate Program Change 2022-23

## I. General Information

The faculty member originating this proposal is to complete sections I and II.

Before starting this form, please review graduate curriculum website on policies and processes: <https://www.unlv.edu/graduatecollege/curriculum>

Click "**validate and launch proposal**" **button below.** Once the pop-up window comes up and displays an error message, click on the "show me" button (on the pop-up). This will highlight in red the required fields of this form. Fill in **ONLY** the red (required) fields.

With all red fields completed, click on the "**validate and launch proposal**" **button** again to launch the proposal. The pop-up this time should not display an error, and will display instead a "**launch proposal**" **button.** Click on the "**launch proposal**" **button** to launch.

**After launch, please fill in all remaining fields in the form to reflect your intended changes, including edits to the schema section of this form (further explained below).**

Department (s) (if  
Dual or  
Interdisciplinary  
please add all  
departments):\*

Electrical and Computer Engineering

Degree or Certificate  
Name:\*

Doctor of Philosophy - Electrical Engineering

Degree Type:\*

Doctor of Philosophy

Program Type:\*

Doctoral

## II. Program Changes

Proposed New CIP  
Code (if applicable):

Are you changing  
admission  
requirements?\*

Yes

No

Are you changing program learning objectives?  Yes  No \*

If yes, describe changes to learning objectives:

Are you changing course requirements?  Yes  No \*

Are you changing degree completion requirements?  Yes  No \*

Are you changing the culminating experience?  Yes (complete the culminating experience section below)  No \*

Other Changes (e.g. subplan titles,...):  Yes  No \*

If not a Dual itself, is this program also available as part of a Dual-Degree offering?  Yes  No \*

## Summary of Changes

**Provide a brief  
summary of proposed  
changes:**

- Adjusted the wording in course sections.
- Changed the area of Solid State Electronics to Solid State Electronics and Photonics.
- Changed the GRE waiver requirement for applicants from ABET-accredited institutes.
- Added the link of the Graduate College English proficiency webpage for international students.
- Added the requirement for a graduate applicant to submit the curriculum vitae listing the applicant's project, publication, and award records.
- Added ECG 703 as an option for students.
- Added ECG 682 as an option for students.
- Added the new course ECG 614 in the area of Solid State Electronics and Photonics.
- Added the new course ECG 712 in the area of Solid State Electronics and Photonics.
- Added the new course ECG 715 in the area of Solid State Electronics and Photonics.
- Added the new course ECG 716 in the area of Solid State Electronics and Photonics.
- In qualifying exam subfields, removed the subfield of Digital Electronics and VLSI, added the subfield Embedded Systems, and removed Organization from the subfield of Computer Architectures.

**Provide a rationale for each proposed change:**

- Adjustment of wording in course sections to be more clear of advisor approval and courses being suggestive only.
- Study of photonics was demanded by students. Faculty voted to add this sub-area into Solid State Electronics.
- GRE only tests basic math, verbal, and analysis skills. For students with a good GPA and research experience, GRE should be waived.
- To keep the English proficiency requirement consistent with Graduate College, it's better to link the webpage directly.
- It is important for a graduate applicant to submit the curriculum vitae listing the applicant's project, publication, and award records.
- ECG 703 course is ready for scheduling and we are just displaying in the suggestive list of courses on the catalog.
- ECG 682 course is ready for scheduling and we are just displaying in the suggestive list of courses on the catalog.
- ECG 614 is a new graduate course to be added in the area of Solid State Electronics and Photonics.
- ECG 712 is a new graduate course to be added in the area of Solid State Electronics and Photonics.
- ECG 715 is a new graduate course to be added in the area of Solid State Electronics and Photonics.
- ECG 716 is a new graduate course to be added in the area of Solid State Electronics and Photonics.
- Rationale: Digital Electronics and VLSI is overlapped with the other subfield Electronics. Several courses in embedded systems were offered. Students should be given the option to be tested in this subfield. Organization was not part of the course name.

**Office of Online Education**

Programs that are 100% online must contact the Office of Online Education (<https://www.unlv.edu/provost/online-education>).

Email: [elizabeth.barrie@unlv.edu](mailto:elizabeth.barrie@unlv.edu)

**What is the current delivery/mode of instruction for this program? \***

- 100% face-to-face courses
- Hybrid (some online courses; some face-to-face courses)
- 100% online courses

**Are you changing the delivery/mode of instruction? (select new mode):\***

- 100% face-to-face courses
- Hybrid (some online courses; some face-to-face courses)
- 100% online courses (contact office of online education)
- No Changes

## Required Additional Documents:

Please attach required documents by navigating to the “attach a file” icon at the right of this form.

Information and forms available at the [Office of the Vice Provost for Academic Programs](#) website.

### If changing to 100% online delivery, please attach the following:

Assessment Plan - A new assessment plan will be required when adding an exclusively online mode of delivery to a degree and it must be approved by the [Office of Academic Assessment](#), [assessment@unlv.edu](mailto:assessment@unlv.edu),

Dean's memo of support

Teach Out Plan (available at the [Office of the Vice Provost for Academic Programs](#) website).

If 100% Online,  
attach the required  
documents and mark  
the checkboxes:

- Assessment Plan
- Dean's memo of support
- Teach Out Plan

## Office of Educational Compliance

Programs that lead to professional licensure or certification must contact the Office of Educational Compliance (<https://www.unlv.edu/provost/ed-compliance>).

Email: [leeann.fields@unlv.edu](mailto:leeann.fields@unlv.edu)

With this change,  
does this program  
lead to professional  
licensure or  
certification?\*

- Yes (contact office of educational compliance)
- No

## SCHEMA SECTION

**Please edit the schema section after launching the proposal. To launch, please complete all other fields of this form and click "validate and launch" at the bottom. If an error message pops up, please click "show me" and fill in all red (missing) fields in the form and click "validate and launch" again until successfully launched.**

In this section, please use the instructions below to change the graduate catalog display of program requirements: descriptions, admissions, courses, degree completion, graduation, etc.

Please note that the new graduate catalog will display the exact information that you edit in this section.

### INSTRUCTIONS:

1. Click on the "View Curriculum Schema" icon at the bottom of the "Schema Section."
2. If you are only adjusting existing content, click on the respective Core you would like to edit, then make your edits and click on "Save" as you go. If you are deleting a subplan or courses, simply delete the respective Cores or courses and adjust the descriptive content accordingly.

If you are adding new subplans and/or adding or substituting courses, please see the steps below:

**Prior to following the steps below, please open in parallel a structurally similar program from the graduate catalog as a reference ([UNLV Graduate Catalog](#)). You will use this reference to review the sections of a program plan, and have direction on expected content.**

3. Click "Add Core" to create blank "Cores." Create as many Cores as you will need. If unsure, refer to other subplans already in this program or to a reference in the graduate catalog to understand how many Cores you need and their expected content.
4. Click on each Core and rename them following your needs or a catalog reference, clicking on "Save" as you go.
5. Add content to all of the cores by clicking within the field to be edited and clicking "Save" as you go. Please note that course sections require instructions of how the credits must be taken (e.g. "Complete x credits by completing all of the following courses," or "Complete x credits of advisor-approved courses," or "Complete 3 credits of course-x and 6 credits of course-z," etc.).
6. To add courses, click on the "View Curriculum Courses" tab at the top of the schema window below, then click on the "Import Course" button at the bottom of the "View Curriculum Courses" window. Click on the available import catalog (only one will be available), and on the import window, select "filter by prefix." (1) Search for your desired course prefix; (2) then click "search available curriculum; (3) then click on all courses that will be imported. Add courses to proposal by clicking, at the very bottom, on the "Add Courses to Proposal" button. Repeat this process as often as you need until all courses are added to the proposal.
7. For courses that do not exist yet at the time of this proposal: Add the courses into the description box of the respective course section in the following way: "PREFIX - NUMBER - Course name, (credits)."
8. Add the courses into their respective course section by clicking "add course" within each course section.

**QUESTIONS?** Step-by-step guides are available at the [Graduate Curriculum Website](#), and at any time please contact [gradcurriculum@unlv.edu](mailto:gradcurriculum@unlv.edu)

## Plan Description

The culminating experience in the Ph.D. program in the Department of Electrical and Computer Engineering is centered about developing new knowledge focused around a specific theme embodied in the form a well-written and orally defended dissertation. The Department of Electrical and Computer Engineering at UNLV offers a number of program options leading to the Ph.D. degree in the Field of Electrical Engineering. Specific major areas of study currently available include: Communications, Computer Engineering, Control System Theory, Electromagnetics and Optics, Electronics, Power Systems, Signal Processing, and Solid State Materials and Devices.

Applicants may be admitted to the Electrical Engineering Ph.D Program through one of the following three options:

- the Post-Master's subplan,
- the Post-Bachelor's subplan,
- or the Post-Bachelor's Integrated BS-PHD subplan.

The Post-Master's subplan requires the student to have completed a M.S. Degree in Electrical Engineering, Computer Engineering, or a closely related field with thesis before entering the program.

The Post-Bachelor's subplan allows undergraduates with outstanding undergraduate backgrounds to enter the Ph.D. program without having to complete a M.S. Degree in Electrical or Computer Engineering.

The Post-Bachelor's Integrated BS-PHD subplan allows students who applied up to 9 credits of graduate courses towards their B.S. degrees to complete their Ph.D. in engineering with up to 9 fewer credits than students in the Post-Bachelor's subplan. All requirements leading to a Ph.D. are still required beyond the B.S. Degree in Electrical and Computer Engineering excluding the completion of a Master's thesis. In conjunction with these options, a dual degree option does exist for candidates simultaneously working towards a Ph.D. degree in Electrical Engineering and a Master of Science degree in Mathematics.

This program prepares graduate students with complementing educational components covering electrical engineering and mathematics, which is the basis of all engineering. [Refer to the Dual Degree Doctor of Philosophy – Electrical Engineering and Master of Science – Mathematical Sciences program description.

**For more information about your program, including your graduate program handbook and learning outcomes, please visit the [Degree Directory](#).**



## Plan Admission Requirements

### [Application deadlines](#)

Applications available on the [UNLV Graduate College website](#).

Applicants are considered on an individual basis. Applicants may be admitted as a regular or provisional status student. Qualified applicants who are not admitted can take graduate courses as a non-degree seeking graduate student. Up to 15 UNLV credits taken as a non-degree seeking graduate student at UNLV can be applied towards a PhD degree program in electrical and computer engineering. Potentially, nine graduate credits taken at another regionally accredited university may be transferred in the PhD degree program at UNLV. At most, only 15 credits of a combination of UNLV and non-UNLV course credits, within the constraints above, may be applied to the PhD program. Courses with a grade less than B (3.0) will not be applied to the PhD program. Further, the courses must not have been or will be applied to different degree program. Note that informal course credits will not be transferred into a PhD degree program. Informal courses such as Graduate Independent Study and Seminar taken as a non-degree seeking student cannot be applied towards a program degree in ECE. Non-degree seeking students can count Electrical & Computer Engineering Graduate Special Topics towards the program degree as long as they adhere to the conditions of the particular program option regarding informal course credits.

To be considered for admission an applicant must:

Submit GRE scaled and percentile scores in quantitative, verbal reasoning, and analytical writing to the Department of Electrical and Computer Engineering and have obtained the following minimum relative percentile comparison rank of 75 in the Quantitative section, 20 in the Verbal Reasoning section, and 20 in Analytical Writing. Please note that GRE scores will only be considered valid if taken within five years prior to the time of admission and are recognized by the GRE examination board. Official scores must be obtained from an official GRE provider. The GRE requirement can be waived under the circumstances listed in the GRE Waiver section.

Submit a completed application form and official transcripts of all college level work to the Graduate College. Submit an additional set of transcripts of all college-level work directly to the Department of Electrical and Computer Engineering.

Submit a one-page written statement of purpose indicating the applicant's interests, motivations, and objectives. In the statement of purpose, the applicant must explicitly identify their areas of interest from the following list of areas offered at UNLV in the ECE Department: Communications, Computer Engineering, Control Systems, Electromagnetics and Optics, Electronics, Power Systems, Signal Processing, and Solid-State Electronics and Photonics (which includes Nanotechnology). Applicants are required to account for all time beyond the B.S. degree indicating how they have developed professionally. Applicants transferring from other graduate programs

without obtaining an M.S. degree must justify why they are leaving that program to join our graduate program. Applicants receiving grades

less than B in a graduate course elsewhere may not be admitted to the graduate program without a well-justified explanation. Poor performance in course work in the program that the student is transferring from can be a cause for denial of admission. It will be the graduate committee's discretion whether to allow or deny admission. Submit a one-page curriculum vitae listing the applicant's project, publication, and award records.

Submit three letters of recommendation (signed and dated) concerning the applicant's potential for succeeding in the graduate program directly to the Department of Electrical and Computer Engineering.

Letters of reference may be electronically uploaded in the online admissions application process. If the student received a M.S. degree in electrical or computer engineering at UNLV, then only one letter of recommendation is required, and it must come from the candidate's faculty advisor who should be the student's thesis committee chair. If the applicant has attended a university or is currently enrolled in a program beyond the M.S. degree, then at least one letter of recommendation should be solicited from that university or program and two from the university in which the M.S. degree was received.

One of the three letters should be written by your thesis advisor commenting on your background and your thesis research. If the applicant has been out of school for an extended period of time, then letters should be solicited from the professional community who can comment on the applicant's technical background and/or from the applicant's most recent academic institution. Letters of recommendation written beyond a six-month period prior to applying for admission to our graduate program will not be accepted. Strong letters of recommendation illustrate technical talent and professional accomplishments beyond the grade point average or course grade. The graduate committee is interested in the applicant's technical, conceptual, verbal, ethical and social skills. The graduate committee is interested in the applicant's ability to perform research with evidence to substantiate claims made. Note that letters from professors that casually know you will not help you in the admission process.

International applicants must provide proof of English proficiency, as stated in the [Graduate College English proficiency requirements website](#).

All domestic and international applicants must review and follow the [Graduate College Admission and Registration Requirements](#).

Application deadlines are February 1st for admission in the fall of the same year and October 1st for admission in the spring of the subsequent year.

Have a Master of Science (M.S.) degree in electrical engineering or computer engineering or a closely related field with an M.S. thesis component. The M.S. thesis must be completed prior to admission. Potential candidates applying to the program based on a course only option or a project option will not be admitted. (Applicants who possess a bachelor's degree in a closely related discipline, such as physics or mathematics, may be admitted on conditional and/or provisional status. These students will be required to complete certain undergraduate and/or graduate courses before they can attain regular full graduate standing status. The graduate committee determines these courses on an individual basis.)

Have a minimum overall grade point average (GPA) of 3.20 (A = 4.00) for their M.S. degree and a 3.00 for their B.S. degree.

***Post-Bachelor's subplan:***

Have a Bachelor of Science (B.S.) degree in electrical engineering or computer engineering or a closely related field. (Applicants who possess a bachelor degree in a closely related discipline, such as physics or mathematics, may be admitted on conditional and/or provisional status. These students will be required to complete certain undergraduate and/or graduate courses before they can attain regular full graduate standing status. The graduate committee determines these courses on an individual basis.)

Have a minimum overall grade point average (GPA) of 3.50 (A = 4.00) for their B.S. degree in Electrical or Computer Engineering a closely related field.

***Post-Bachelor's Integrated BS-PHD subplan:***

The Integrated BS-PHD *subplan* program allows UNLV undergraduate students who applied up to 9 credits of UNLV electrical engineering or computer engineering graduate courses towards their UNLV B.S. in Electrical Engineering or Computer Engineering degree to complete their Ph.D. in engineering with up to 9 fewer credits than students in the Post-Bachelor's *subplan*. All requirements leading to a Ph.D. are still required beyond the B.S. Degree in Electrical and Computer Engineering excluding the completion of a Master's thesis.

Have a minimum overall grade point average (GPA) of 3.5 (A = 4.00) for their B.S. degree in electrical engineering or computer engineering at UNLV.

Have completed up to a maximum of 9 credits of formal Graduate College curriculum approved 600/700 level courses (which excludes informal courses such as Graduate Independent Study, Graduate Seminar, and Special Topics) which were applied towards the student's B.S. degree. Each graduate level course must have been completed with a minimum grade of B (GPA) of 3.2 (A = 4.00).

### **GRE Waiver:**

The GRE entrance requirement will be waived for students entering the Ph.D. program if ***ALL*** of the following are satisfied. The final decision is up to the graduate committee.

1. The candidate received a bachelor or master's degree (thesis option) in electrical and/or computer engineering from an ABET-accredited electrical and/or computer engineering program.
2. The candidate's BS GPA equals or exceeds 3.0 and MS GPA equals or exceeds 3.6 for conventional Ph. D. applicants. The candidate's BS GPA equals or exceeds 3.5 for direct Ph. D. applicants.
3. The conventional Ph. D. applicant must show evidence that a paper pertaining to his/her research has been accepted/published in a refereed conference (minimum requirement) or a journal. Reviewers' comments along with a letter of acceptance may be used as minimum evidence that a paper will be published. The direct Ph. D. applicant must show evidence of research experience. Examples of such evidence include but not limited to 1) a research publication; 2) a research poster/presentation; 3) participation in a sponsored research project; 4) participation in a research internship, etc.
4. Letters of recommendation should clearly indicate the student's potential and ability to succeed in research and higher education.

*Students are accepted into a degree program as described in the Graduate Catalog. The faculty and corresponding sub-disciplines and sub-plans within the described programs are subject to change at any time.*

## **Plan Requirements**

*See Subplan Requirements below.*

[Subplan 1: Post-Master's](#)

[Subplan 2: Post-Bachelor's](#)

[Subplan 3: Post-Bachelor's Integrated BS-PHD](#)

## **Subplan 1 Requirements: Post-Master's**

**Total Credits Required: 45**

## Course Requirements

### Major Field Courses – Credits: 6-15

Complete 6-15 credits of advisor approved coursework in an single major area in Electrical and Computer Engineering (suggested courses below), with a minimum overall average GPA of 3.33.

### Communications

<b>ECG 662 Digital Communication Systems</b>	<b>3</b>
<b>ECG 666 Wireless and Mobile Communication Systems</b>	<b>3</b>
<b>ECG 704 Coding with Applications in Computers and Communication Media</b>	<b>3</b>
<b>ECG 706 Analysis of Telecommunication and Data Networks</b>	<b>3</b>
<b>ECG 760 Random Processes in Engineering Problems</b>	<b>3</b>
<b>ECG 762 Detection and Estimation of Signals in Noise</b>	<b>3</b>
<b>ECG 763 Advanced Digital Communication Systems</b>	<b>3</b>

### Computer Engineering

<b>ECG 600 Computer Communication Networks</b>	<b>3</b>
<b>ECG 603 Embedded Systems Design</b>	<b>3</b>
<b>ECG 604 Modern Processor Architecture</b>	<b>3</b>
<b>ECG 605 Data Compression Systems</b>	<b>3</b>
<b>ECG 607 Biometrics</b>	<b>3</b>
<b>ECG 608 Digital Design Verification and Testing</b>	<b>3</b>
<b>ECG 609 Embedded Digital Signal Processing</b>	<b>3</b>
<b>ECG 617 Internet of Things Systems</b>	<b>3</b>
<b>ECG 700 Advanced Computer System Architecture</b>	<b>3</b>
<b>ECG 701 Reliable Design of Digital Systems</b>	<b>3</b>
<b>ECG 702 Interconnection Networks for Parallel Processing Applications</b>	<b>3</b>
<b>ECG 703 Machine Learning and Applications</b>	<b>3</b>
<b>ECG 704 Codina with Applications in Computers</b>	<b>3</b>

## Engineering Applications in Computers and Communication Media

<b>ECG 706 Analysis of Telecommunication and Data Networks</b>	<b>3</b>
<b>ECG 707 Logic Synthesis Engineering</b>	<b>3</b>
<b>ECG 709 Synthesis and Optimization of Digital Systems</b>	<b>3</b>

## Control Systems Theory

<b>ECG 672 Digital Control Systems</b>	<b>3</b>
<b>ECG 770 Linear Systems</b>	<b>3</b>
<b>ECG 771 Optimal and Modern Control</b>	<b>3</b>
<b>ECG 772 Nonlinear Systems</b>	<b>3</b>
<b>ECG 774 Stochastic Control</b>	<b>3</b>
<b>ECG 776 Adaptive Control</b>	<b>3</b>

## Electromagnetics and Optics

<b>ECG 630 Transmission Lines</b>	<b>3</b>
<b>ECG 631 Engineering Optics</b>	<b>3</b>
<b>ECG 632 Antenna Engineering</b>	<b>3</b>
<b>ECG 633 Active and Passive Microwave Engineering</b>	<b>3</b>
<b>ECG 730 Advanced Engineering Electromagnetics I</b>	<b>3</b>
<b>ECG 731 Theoretical Techniques in Electromagnetics</b>	<b>3</b>
<b>ECG 732 Advanced Engineering Electromagnetics II</b>	<b>3</b>
<b>ECG 733 Plasma I</b>	<b>3</b>

## Electronics

<b>ECG 620 Analog Integrated Circuit Design</b>	<b>3</b>
<b>ECG 621 Digital Integrated Circuit Design</b>	<b>3</b>
<b>ECG 720 Advanced Analog IC Design</b>	<b>3</b>
<b>ECG 721 Memory Circuit Design</b>	<b>3</b>
<b>ECG 722 Mixed-Signal Circuit Design</b>	<b>3</b>

## Power Engineering

<b>ECG 642 Power Electronics</b>	<b>3</b>
<b>ECG 646 Photovoltaic Devices and Systems</b>	<b>3</b>
<b>ECG 740 Computer Analysis Methods for Power Systems</b>	<b>3</b>
<b>ECG 741 Electric Power Distribution System Engineering</b>	<b>3</b>
<b>ECG 742 Power System Stability and Control</b>	<b>3</b>
<b>ECG 743 Smart Electrical Power Grid</b>	<b>3</b>

## **Signal Processing**

<b>ECG 680 Discrete-Time Signal Processing</b>	<b>3</b>
<b>ECG 682 Introduction to Biomedical Signals and Systems</b>	<b>3</b>
<b>ECG 703 Machine Learning and Applications</b>	<b>3</b>
<b>ECG 760 Random Processes in Engineering Problems</b>	<b>3</b>
<b>ECG 762 Detection and Estimation of Signals in Noise</b>	<b>3</b>
<b>ECG 781 Digital Filters</b>	<b>3</b>
<b>ECG 782 Multidimensional Digital Signal Processing</b>	<b>3</b>
<b>ECG 783 Adaptive Signal Processing with Neural Networks</b>	<b>3</b>

## **Solid State Electronics and Photonics**

<b>[Before] ECG - 614 - Quantum Communication</b>	<b>3</b>
<b>ECG 651 Electronic and Magnetic Materials and Devices</b>	<b>3</b>
<b>ECG 652 Optoelectronics</b>	<b>3</b>
<b>ECG 653 Introduction to Nanotechnology</b>	<b>3</b>
<b>[After] ECG - 712 - Optical Materials and Devices</b>	<b>3</b>
<b>[After] ECG - 715 - Biomedical Photonics</b>	<b>3</b>
<b>[After] ECG - 716 - Space Systems</b>	<b>3</b>
<b>ECG 750 Photonics</b>	<b>3</b>
<b>ECG 752 Physical Electronics</b>	<b>3</b>
<b>ECG 753 Advanced Topics in Semiconductor Devices I</b>	<b>3</b>
<b>ECG 755 Monolithic Integrated Circuit Fabrication</b>	<b>3</b>
<b>ECG 756 Advanced Topics in Semiconductor Devices II</b>	<b>3</b>
<b>ECG 757 Electron Transport Phenomena in Solid State Devices</b>	<b>3</b>
<b>ECG 758 Numerical Methods in Engineering</b>	<b>3</b>

## Minor Fields Courses – Credits: 6-18

Select two advisor-approved minor fields and complete coursework (suggested courses below) in each single area totaling 6-18 credits, with a minimum overall average GPA of 3.33. The secondary minor can be from a field outside Electrical Engineering.

### Communications

<b>ECG 662 Digital Communication Systems</b>	<b>3</b>
<b>ECG 666 Wireless and Mobile Communication Systems</b>	<b>3</b>
<b>ECG 760 Random Processes in Engineering Problems</b>	<b>3</b>
<b>ECG 762 Detection and Estimation of Signals in Noise</b>	<b>3</b>

### Computer Engineering

<b>ECG 600 Computer Communication Networks</b>	<b>3</b>
<b>ECG 603 Embedded Systems Design</b>	<b>3</b>
<b>ECG 604 Modern Processor Architecture</b>	<b>3</b>
<b>ECG 605 Data Compression Systems</b>	<b>3</b>
<b>ECG 607 Biometrics</b>	<b>3</b>
<b>ECG 608 Digital Design Verification and Testing</b>	<b>3</b>
<b>ECG 609 Embedded Digital Signal Processing</b>	<b>3</b>
<b>ECG 617 Internet of Things Systems</b>	<b>3</b>
<b>[After] ECG 618 Cloud Computing in Engineering</b>	<b>3</b>
<b>ECG 700 Advanced Computer System Architecture</b>	<b>3</b>
<b>ECG 701 Reliable Design of Digital Systems</b>	<b>3</b>
<b>ECG 702 Interconnection Networks for Parallel Processing Applications</b>	<b>3</b>
<b>ECG 703 Machine Learning and Applications</b>	<b>3</b>
<b>ECG 704 Coding with Applications in Computers and Communication Media</b>	<b>3</b>
<b>ECG 706 Analysis of Telecommunication and Data Networks</b>	<b>3</b>
<b>ECG 707 Logic Synthesis Engineering</b>	<b>3</b>
<b>ECG 709 Synthesis and Optimization of Digital Systems</b>	<b>3</b>

### Control Systems Theory



<b>ECG 770 Linear Systems</b>	<b>3</b>
<b>ECG 771 Optimal and Modern Control</b>	<b>3</b>
<b>ECG 772 Nonlinear Systems</b>	<b>3</b>
<b>ECG 774 Stochastic Control</b>	<b>3</b>
<b>ECG 776 Adaptive Control</b>	<b>3</b>

## **Electromagnetics and Optics**

<b>ECG 630 Transmission Lines</b>	<b>3</b>
<b>ECG 631 Engineering Optics</b>	<b>3</b>
<b>ECG 632 Antenna Engineering</b>	<b>3</b>
<b>ECG 633 Active and Passive Microwave Engineering</b>	<b>3</b>
<b>ECG 730 Advanced Engineering Electromagnetics I</b>	<b>3</b>
<b>ECG 731 Theoretical Techniques in Electromagnetics</b>	<b>3</b>
<b>ECG 732 Advanced Engineering Electromagnetics II</b>	<b>3</b>
<b>ECG 733 Plasma I</b>	<b>3</b>

## **Electronics**

<b>ECG 620 Analog Integrated Circuit Design</b>	<b>3</b>
<b>ECG 621 Digital Integrated Circuit Design</b>	<b>3</b>
<b>ECG 720 Advanced Analog IC Design</b>	<b>3</b>
<b>ECG 721 Memory Circuit Design</b>	<b>3</b>
<b>ECG 722 Mixed-Signal Circuit Design</b>	<b>3</b>

## **Power Engineering**

<b>ECG 642 Power Electronics</b>	<b>3</b>
<b>ECG 646 Photovoltaic Devices and Systems</b>	<b>3</b>
<b>ECG 740 Computer Analysis Methods for Power Systems</b>	<b>3</b>
<b>ECG 741 Electric Power Distribution System Engineering</b>	<b>3</b>
<b>ECG 742 Power System Stability and Control</b>	<b>3</b>
<b>ECG 743 Smart Electrical Power Grid</b>	<b>3</b>

## **Signal Processing**

<b>ECG 680 Discrete-Time Signal Processing</b>	<b>3</b>
<b>ECG 682 Introduction to Biomedical Signals and Systems</b>	<b>3</b>
<b>ECG 703 Machine Learning and Applications</b>	<b>3</b>
<b>ECG 760 Random Processes in Engineering Problems</b>	<b>3</b>
<b>ECG 762 Detection and Estimation of Signals in Noise</b>	<b>3</b>
<b>ECG 781 Digital Filters</b>	<b>3</b>
<b>ECG 782 Multidimensional Digital Signal Processing</b>	<b>3</b>
<b>ECG 783 Adaptive Signal Processing with Neural Networks</b>	<b>3</b>

## **Solid State Electronics and Photonics**

<b>[Before] ECG - 614 - Quantum Communication</b>	<b>3</b>
<b>ECG 651 Electronic and Magnetic Materials and Devices</b>	<b>3</b>
<b>ECG 652 Optoelectronics</b>	<b>3</b>
<b>ECG 653 Introduction to Nanotechnology</b>	<b>3</b>
<b>[After] ECG - 712 - Optical Materials and Devices</b>	<b>3</b>
<b>[After] ECG - 715 - Biomedical Photonics</b>	<b>3</b>
<b>[After] ECG - 716 - Space Systems</b>	<b>3</b>
<b>ECG 750 Photonics</b>	<b>3</b>
<b>ECG 752 Physical Electronics</b>	<b>3</b>
<b>ECG 753 Advanced Topics in Semiconductor Devices I</b>	<b>3</b>
<b>ECG 755 Monolithic Integrated Circuit Fabrication</b>	<b>3</b>
<b>ECG 756 Advanced Topics in Semiconductor Devices II</b>	<b>3</b>
<b>ECG 757 Electron Transport Phenomena in Solid State Devices</b>	<b>3</b>
<b>ECG 758 Numerical Methods in Engineering</b>	<b>3</b>

## **Elective Courses – Credits: 0-15**

Complete 0-15 credits of 600- or 700-level MAT, PHY, AST, CEE, CEM, ECG, EGG, CS, ME, or other advisor-approved courses.

## **Dissertation – Credits: 18**

**ECG 799 Dissertation**

**1 – 6**

## **Degree Requirements**

All Ph.D. students must satisfy the Ph.D. degree program admission requirements and be admitted to the Ph.D. program on a regular status.

Complete a minimum of 27 credits of graduate level courses (excluding dissertation credits) with an overall minimum GPA of 3.20 and a minimum GPA of 2.70 (B-) in each class applied towards the 27 credits. The final division of major, minor, and elective credits will be determined in consultation with the student's advisor.

Of the 27 required credits, a minimum of 18 credits must be in 700-level courses. Of these 18 credits, a minimum of 15 must be from formal courses. The student's doctoral advisory committee may add more requirements in accordance with the individual's background and field of study.

No more than 3 credits may be from Graduate Independent Study together with Graduate Seminar. No more than 6 credits of a combination of informal courses such as Graduate Independent Study, Special Topics, and Seminar may be applied to the degree program.

Beyond the Bachelor degree, a Ph.D. student must complete a minimum of 15 credits in an approved ECE major field, 9 credits an approved ECE minor (primary minor) field, and 9 credits in a second approved open minor (secondary minor) field. Of the 15 credits required in the ECE major field, a minimum of 9 credits must be completed in 700-level courses. A minimum GPA of 3.33 (B+=3.30) must be obtained in the major field. Of the 9 required credits in each minor field, a minimum of 6 credits must be in 700-level courses. A minimum GPA of 3.33 (B+=3.30) must be obtained in each of the minor fields.

Informal courses (Graduate Independent Study, Graduate Seminar, and Special Topics) cannot be applied to the ECE major, ECE minor (primary minor) and the open minor (secondary minor) fields.

At the time of admission or no later than the first semester, the Ph.D. candidate must formally petition BOTH the graduate college and the ECE graduate committee to accept transfer credits and credits taken as a non-degree seeking graduate student to be applied to the Ph.D. program.

All regular (full graduate standing) status graduate students must select a faculty advisor in their first semester.

Maintain a minimum overall grade point average (GPA) of 3.20, must maintain a minimum GPA of 3.20 each semester, and must complete all graduate level courses that apply towards their degree with a minimum GPA of 2.70 (B-) in each course. Grades below B- cannot be applied towards the Ph.D. degree and must be repeated or replaced. A class grade below C (2.0) is grounds for initiating a program separation recommendation to the Graduate College. Ph.D. candidates who do not maintain an overall minimum GPA of 3.2, who do not maintain a minimum GPA of 3.2 each semester, or who earn more than one grade

below B- may be placed on academic probation or expelled from the program. The Electrical and Computer Engineering Graduate

Committee in conjunction with the Graduate College will determine the terms of the student's probation based upon the student's academic record and in accordance with the rules of the Graduate College.

All regular (full graduate standing) status graduate students must file an approved Plan of Study form before the completion of their third semester. This program must be approved by the student's advisor and the graduate coordinator. All regular and provisional status graduate students must show satisfactory progress towards completion of their degree by completing at least six credits of their approved program per calendar year. If their progress towards their degree program is not satisfactory, students may either be put on probation or expelled from the program.

Before beginning a dissertation, students must have their dissertation topic approved by their advisor, and the necessary paper work including a dissertation prospectus must be filed with the Graduate College by the end of the third semester. The dissertation prospectus describes the dissertation topic and must include an introductory set of sentences, a well formed hypothesis or hypotheses (specifically italicized in the prospectus) accompanied by a motivation, objectives with major and alternative approaches to the studies, and conjectures of possible outcomes. Students are NOT allowed to take dissertation credits until their prospectus is approved. Credits taken before the approval date will NOT count towards the degree program.

## **Graduation Requirements**

[See Plan Graduation Requirements below.](#)

## **Subplan 2 Requirements: Post-Bachelor's**

Total Credits Required: 69

## **Course Requirements**

## **Major Field Courses – Credits: 15**

Complete 15 credits of advisor approved coursework in an single major area in Electrical and Computer Engineering (suggested courses below), with a minimum overall GPA of 3.33. A minimum of 9 credits must be in 700-level courses.

## Communications

<b>ECG 662 Digital Communication Systems</b>	<b>3</b>
<b>ECG 666 Wireless and Mobile Communication Systems</b>	<b>3</b>
<b>ECG 704 Coding with Applications in Computers and Communication Media</b>	<b>3</b>
<b>ECG 706 Analysis of Telecommunication and Data Networks</b>	<b>3</b>
<b>ECG 760 Random Processes in Engineering Problems</b>	<b>3</b>
<b>ECG 762 Detection and Estimation of Signals in Noise</b>	<b>3</b>
<b>ECG 763 Advanced Digital Communication Systems</b>	<b>3</b>

## Computer Engineering

<b>ECG 600 Computer Communication Networks</b>	<b>3</b>
<b>ECG 603 Embedded Systems Design</b>	<b>3</b>
<b>ECG 604 Modern Processor Architecture</b>	<b>3</b>
<b>ECG 605 Data Compression Systems</b>	<b>3</b>
<b>ECG 607 Biometrics</b>	<b>3</b>
<b>ECG 608 Digital Design Verification and Testing</b>	<b>3</b>
<b>ECG 609 Embedded Digital Signal Processing</b>	<b>3</b>
<b>ECG 617 Internet of Things Systems</b>	<b>3</b>
<b>ECG 700 Advanced Computer System Architecture</b>	<b>3</b>
<b>ECG 701 Reliable Design of Digital Systems</b>	<b>3</b>
<b>ECG 702 Interconnection Networks for Parallel Processing Applications</b>	<b>3</b>
<b>ECG 703 Machine Learning and Applications</b>	<b>3</b>
<b>ECG 704 Coding with Applications in Computers and Communication Media</b>	<b>3</b>
<b>ECG 706 Analysis of Telecommunication and Data Networks</b>	<b>3</b>
<b>ECG 707 Logic Synthesis Engineering</b>	<b>3</b>
<b>ECG 709 Synthesis and Optimization of Digital Systems</b>	<b>3</b>

## Control Systems Theory

<b>ECG 672 Digital Control Systems</b>	<b>3</b>
<b>ECG 770 Linear Systems</b>	<b>3</b>
<b>ECG 771 Optimal and Modern Control</b>	<b>3</b>
<b>ECG 772 Nonlinear Systems</b>	<b>3</b>
<b>ECG 774 Stochastic Control</b>	<b>3</b>
<b>ECG 776 Adaptive Control</b>	<b>3</b>

## **Electromagnetics and Optics**

<b>ECG 630 Transmission Lines</b>	<b>3</b>
<b>ECG 631 Engineering Optics</b>	<b>3</b>
<b>ECG 632 Antenna Engineering</b>	<b>3</b>
<b>ECG 633 Active and Passive Microwave Engineering</b>	<b>3</b>
<b>ECG 730 Advanced Engineering Electromagnetics I</b>	<b>3</b>
<b>ECG 731 Theoretical Techniques in Electromagnetics</b>	<b>3</b>
<b>ECG 732 Advanced Engineering Electromagnetics II</b>	<b>3</b>
<b>ECG 733 Plasma I</b>	<b>3</b>

## **Electronics**

<b>ECG 620 Analog Integrated Circuit Design</b>	<b>3</b>
<b>ECG 621 Digital Integrated Circuit Design</b>	<b>3</b>
<b>ECG 720 Advanced Analog IC Design</b>	<b>3</b>
<b>ECG 721 Memory Circuit Design</b>	<b>3</b>
<b>ECG 722 Mixed-Signal Circuit Design</b>	<b>3</b>

## **Power Engineering**

<b>ECG 642 Power Electronics</b>	<b>3</b>
<b>ECG 646 Photovoltaic Devices and Systems</b>	<b>3</b>
<b>ECG 740 Computer Analysis Methods for Power Systems</b>	<b>3</b>
<b>ECG 741 Electric Power Distribution System Engineering</b>	<b>3</b>
<b>ECG 742 Power System Stability and Control</b>	<b>3</b>
<b>ECG 743 Smart Electrical Power Grid</b>	<b>3</b>

## **Signal Processing**

<b>ECG 680 Discrete-Time Signal Processing</b>	<b>3</b>
<b>ECG 682 Introduction to Biomedical Signals and Systems</b>	<b>3</b>
<b>ECG 703 Machine Learning and Applications</b>	<b>3</b>
<b>ECG 760 Random Processes in Engineering Problems</b>	<b>3</b>
<b>ECG 762 Detection and Estimation of Signals in Noise</b>	<b>3</b>
<b>ECG 781 Digital Filters</b>	<b>3</b>
<b>ECG 782 Multidimensional Digital Signal Processing</b>	<b>3</b>
<b>ECG 783 Adaptive Signal Processing with Neural Networks</b>	<b>3</b>

## **Solid State Electronics and Photonics**

<b>[After] ECG - 614 - Quantum Communication</b>	<b>3</b>
<b>ECG 651 Electronic and Magnetic Materials and Devices</b>	<b>3</b>
<b>ECG 652 Optoelectronics</b>	<b>3</b>
<b>ECG 653 Introduction to Nanotechnology</b>	<b>3</b>
<b>[After] ECG - 712 - Optical Materials and Devices</b>	<b>3</b>
<b>[After] ECG - 715 - Biomedical Photonics</b>	<b>3</b>
<b>[After] ECG - 716 - Space Systems</b>	<b>3</b>
<b>ECG 750 Photonics</b>	<b>3</b>
<b>ECG 752 Physical Electronics</b>	<b>3</b>
<b>ECG 753 Advanced Topics in Semiconductor Devices I</b>	<b>3</b>
<b>ECG 755 Monolithic Integrated Circuit Fabrication</b>	<b>3</b>
<b>ECG 756 Advanced Topics in Semiconductor Devices II</b>	<b>3</b>
<b>ECG 757 Electron Transport Phenomena in Solid State Devices</b>	<b>3</b>
<b>ECG 758 Numerical Methods in Engineering</b>	<b>3</b>

## **Minor Fields Courses – Credits: 18**

Select two advisor-approved minor fields and complete 9 credits of coursework (suggested courses below) in each single area with a minimum overall average GPA of 3.33. A minimum of 6 credits in each area must be in 700-level courses. The secondary minor can be from a field outside Electrical Engineering.

## **Communications**

<b>ECG 662 Digital Communication Systems</b>	<b>3</b>
<b>ECG 666 Wireless and Mobile Communication Systems</b>	<b>3</b>
<b>ECG 760 Random Processes in Engineering Problems</b>	<b>3</b>
<b>ECG 762 Detection and Estimation of Signals in Noise</b>	<b>3</b>

## **Computer Engineering**

<b>ECG 600 Computer Communication Networks</b>	<b>3</b>
<b>ECG 603 Embedded Systems Design</b>	<b>3</b>
<b>ECG 604 Modern Processor Architecture</b>	<b>3</b>
<b>ECG 605 Data Compression Systems</b>	<b>3</b>
<b>ECG 607 Biometrics</b>	<b>3</b>
<b>ECG 608 Digital Design Verification and Testing</b>	<b>3</b>
<b>ECG 609 Embedded Digital Signal Processing</b>	<b>3</b>
<b>ECG 617 Internet of Things Systems</b>	<b>3</b>
<b>ECG 700 Advanced Computer System Architecture</b>	<b>3</b>
<b>ECG 701 Reliable Design of Digital Systems</b>	<b>3</b>
<b>ECG 702 Interconnection Networks for Parallel Processing Applications</b>	<b>3</b>
<b>ECG 703 Machine Learning and Applications</b>	<b>3</b>
<b>ECG 704 Coding with Applications in Computers and Communication Media</b>	<b>3</b>
<b>ECG 706 Analysis of Telecommunication and Data Networks</b>	<b>3</b>
<b>ECG 707 Logic Synthesis Engineering</b>	<b>3</b>
<b>ECG 709 Synthesis and Optimization of Digital Systems</b>	<b>3</b>

## **Control Systems Theory**

<b>ECG 770 Linear Systems</b>	<b>3</b>
<b>ECG 771 Optimal and Modern Control</b>	<b>3</b>
<b>ECG 772 Nonlinear Systems</b>	<b>3</b>
<b>ECG 774 Stochastic Control</b>	<b>3</b>
<b>ECG 776 Adaptive Control</b>	<b>3</b>

## **Electromagnetics and Optics**

<b>ECG 630 Transmission Lines</b>	<b>3</b>
<b>ECG 631 Engineering Optics</b>	<b>3</b>
<b>ECG 632 Antenna Engineering</b>	<b>3</b>
<b>ECG 633 Active and Passive Microwave</b>	<b>3</b>



<b>Engineering</b>	
<b>ECG 730 Advanced Engineering Electromagnetics I</b>	<b>3</b>
<b>ECG 731 Theoretical Techniques in Electromagnetics</b>	<b>3</b>
<b>ECG 732 Advanced Engineering Electromagnetics II</b>	<b>3</b>
<b>ECG 733 Plasma I</b>	<b>3</b>

## **Electronics**

<b>ECG 620 Analog Integrated Circuit Design</b>	<b>3</b>
<b>ECG 621 Digital Integrated Circuit Design</b>	<b>3</b>
<b>ECG 720 Advanced Analog IC Design</b>	<b>3</b>
<b>ECG 721 Memory Circuit Design</b>	<b>3</b>
<b>ECG 722 Mixed-Signal Circuit Design</b>	<b>3</b>

## **Power Engineering**

<b>ECG 642 Power Electronics</b>	<b>3</b>
<b>ECG 646 Photovoltaic Devices and Systems</b>	<b>3</b>
<b>ECG 740 Computer Analysis Methods for Power Systems</b>	<b>3</b>
<b>ECG 741 Electric Power Distribution System Engineering</b>	<b>3</b>
<b>ECG 742 Power System Stability and Control</b>	<b>3</b>
<b>ECG 743 Smart Electrical Power Grid</b>	<b>3</b>

## **Signal Processing**

<b>ECG 680 Discrete-Time Signal Processing</b>	<b>3</b>
<b>ECG 682 Introduction to Biomedical Signals and Systems</b>	<b>3</b>
<b>ECG 703 Machine Learning and Applications</b>	<b>3</b>
<b>ECG 760 Random Processes in Engineering Problems</b>	<b>3</b>
<b>ECG 762 Detection and Estimation of Signals in Noise</b>	<b>3</b>
<b>ECG 781 Digital Filters</b>	<b>3</b>
<b>ECG 782 Multidimensional Digital Signal Processing</b>	<b>3</b>
<b>ECG 783 Adaptive Signal Processing with Neural Networks</b>	<b>3</b>

## **Solid State Electronics and Photonics**

[After] ECG - 614 - Quantum Communication	3
ECG 651 Electronic and Magnetic Materials and Devices	3
ECG 652 Optoelectronics	3
ECG 653 Introduction to Nanotechnology	3
[After] ECG - 712 - Optical Materials and Devices	3
[After] ECG - 715 - Biomedical Photonics	3
[After] ECG - 716 - Space Systems	3
ECG 750 Photonics	3
ECG 752 Physical Electronics	3
ECG 753 Advanced Topics in Semiconductor Devices I	3
ECG 755 Monolithic Integrated Circuit Fabrication	3
ECG 756 Advanced Topics in Semiconductor Devices II	3
ECG 757 Electron Transport Phenomena in Solid State Devices	3
ECG 758 Numerical Methods in Engineering	3

### **700-Level Elective Courses – Credits: 12**

Complete 12 credits of 700-level MAT, PHY, AST, CEE, CEM, ECG, EGG, CS, ME, or other advisor-approved courses.

### **Elective Courses – Credits: 6**

Complete 6 credits of 600- or 700-level MAT, PHY, AST, CEE, CEM, ECG, EGG, CS, ME, or other advisor-approved courses.

### **Dissertation – Credits: 18**

ECG 799 Dissertation	1 – 6
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### **Degree Requirements**

All Ph.D. students must satisfy the Ph.D. degree program admission requirements and be admitted to the Ph.D. program on a regular status.

Complete a minimum of 51 credits (24 M.S.E. credits + 27 Post-

**Master's subplan credits) of graduate level courses (excluding**

**dissertation credits) with an overall minimum GPA of 3.20 and a minimum GPA of 2.70 (B-) in each class applied towards the 27 credits. Of the 51 required credits, a minimum of 33 credits must be in 700-level courses. Of these 33 credits, a minimum of 30 must be from formal courses. The student's doctoral advisory committee may add more requirements in accordance with the individual's background and field of study.**

**No more than 6 credits may be from Graduate Independent Study together with Graduate Seminar. No more than 12 credits of a combination of informal courses such as Graduate Independent Study, Special Topics, and Seminar may be applied to the degree program. Complete a minimum of 15 credits in an approved ECE major field, 9 credits an approved ECE minor (primary minor) field, and 9 credits in a second approved open minor (secondary minor) field. Of the 15 credits required in the ECE major field, a minimum of 9 credits must be completed in 700-level courses. A minimum GPA of 3.33 (B+=3.30) must be obtained in the major field. Of the 9 required credits in each minor field, a minimum of 6 credits must be in 700-level courses. A minimum GPA of 3.33 (B+=3.30) must be obtained in each of the minor fields.**

**Informal courses (Graduate Independent Study, Graduate Seminar, and Special Topics) cannot be applied to the ECE major, ECE minor (primary minor) and the open minor (secondary minor) fields.**

**At the time of admission or no later than the first semester, the Ph.D. candidate must formally petition BOTH the graduate college and the ECE graduate committee to accept transfer credits and credits taken as a non-degree seeking graduate student to be applied to the Ph.D. program.**

**All regular (full graduate standing) status graduate students must select a faculty advisor in their first semester.**

**Students on academic probation may be transferred to the M.S.E. Program depending on the student's academic record. In such a case, the M.S.E. Program requirements must be satisfied. For example, only 6 credits of the informal courses may be applied to the M.S.E. degree program with the further constraint that up to 3 credits total of Independent Study in combination with Graduate Seminar may be in the 6 credits.**

**Maintain a minimum overall grade point average (GPA) of 3.20, must maintain a minimum GPA of 3.20 each semester, and must complete all graduate level courses that apply towards their degree with a minimum GPA of 2.70 (B-) in each course. Grades below B- cannot be applied towards the Ph.D. degree and must be repeated or replaced. A class grade below C (2.0) is grounds for initiating a program separation recommendation to the Graduate College. Ph.D. candidates who do not maintain an overall minimum GPA of 3.2, who do not maintain a minimum GPA of 3.2 each semester, or who earn more than one grade**

below B- may be placed on academic probation or expelled from the program. The Electrical and Computer Engineering Graduate

Committee and/or the Graduate College will determine the terms of the student's probation in accordance with the rules of the Graduate College.

All regular status graduate students must file an approved Plan of Study form before the completion of their third semester. This program must be approved by the student's advisor and the graduate coordinator. All regular and provisional status graduate students must show satisfactory progress towards completion of their degree by completing at least six credits of their approved program per calendar year. If their progress towards their degree program is not satisfactory, students may either be put on probation or expelled from the program. Before beginning a dissertation, students must have their dissertation topic approved by their advisor, and the necessary paper work including a dissertation prospectus must be filed with the Graduate College by the end of the third semester. The dissertation prospectus describes the dissertation topic and must include an introductory set of sentences, a well formed hypothesis or hypotheses (specifically italicized in the prospectus) accompanied by a motivation, objectives with major and alternative approaches to the studies, and conjectures of possible outcomes. Students are NOT allowed to take dissertation credits until their prospectus is approved. Credits taken before the approval date will NOT count towards the degree program.

## **Graduation Requirements**

[See Plan Graduation Requirements below.](#)

## **Subplan 3 Requirements: Post-Bachelor's Integrated BS-PHD**

**Total Credits Required: 60-66**

## **Course Requirements**

## **Major Field Courses – Credits: 6-15**

Complete 6-15 credits of advisor approved coursework in an single major area in Electrical and Computer Engineering (suggested courses below), with a minimum overall GPA of 3.33.

## Communications

<b>ECG 662 Digital Communication Systems</b>	<b>3</b>
<b>ECG 666 Wireless and Mobile Communication Systems</b>	<b>3</b>
<b>ECG 704 Coding with Applications in Computers and Communication Media</b>	<b>3</b>
<b>ECG 706 Analysis of Telecommunication and Data Networks</b>	<b>3</b>
<b>ECG 760 Random Processes in Engineering Problems</b>	<b>3</b>
<b>ECG 762 Detection and Estimation of Signals in Noise</b>	<b>3</b>
<b>ECG 763 Advanced Digital Communication Systems</b>	<b>3</b>

## Computer Engineering

<b>ECG 600 Computer Communication Networks</b>	<b>3</b>
<b>ECG 603 Embedded Systems Design</b>	<b>3</b>
<b>ECG 604 Modern Processor Architecture</b>	<b>3</b>
<b>ECG 605 Data Compression Systems</b>	<b>3</b>
<b>ECG 607 Biometrics</b>	<b>3</b>
<b>ECG 608 Digital Design Verification and Testing</b>	<b>3</b>
<b>ECG 609 Embedded Digital Signal Processing</b>	<b>3</b>
<b>ECG 617 Internet of Things Systems</b>	<b>3</b>
<b>ECG 700 Advanced Computer System Architecture</b>	<b>3</b>
<b>ECG 701 Reliable Design of Digital Systems</b>	<b>3</b>
<b>ECG 702 Interconnection Networks for Parallel Processing Applications</b>	<b>3</b>
<b>ECG 703 Machine Learning and Applications</b>	<b>3</b>
<b>ECG 704 Coding with Applications in Computers and Communication Media</b>	<b>3</b>
<b>ECG 706 Analysis of Telecommunication and Data Networks</b>	<b>3</b>
<b>ECG 707 Logic Synthesis Engineering</b>	<b>3</b>
<b>ECG 709 Synthesis and Optimization of Digital Systems</b>	<b>3</b>

## Control Systems Theory

<b>ECG 672 Digital Control Systems</b>	<b>3</b>
<b>ECG 770 Linear Systems</b>	<b>3</b>
<b>ECG 771 Optimal and Modern Control</b>	<b>3</b>
<b>ECG 772 Nonlinear Systems</b>	<b>3</b>
<b>ECG 774 Stochastic Control</b>	<b>3</b>
<b>ECG 776 Adaptive Control</b>	<b>3</b>

## **Electromagnetics and Optics**

<b>ECG 630 Transmission Lines</b>	<b>3</b>
<b>ECG 631 Engineering Optics</b>	<b>3</b>
<b>ECG 632 Antenna Engineering</b>	<b>3</b>
<b>ECG 633 Active and Passive Microwave Engineering</b>	<b>3</b>
<b>ECG 730 Advanced Engineering Electromagnetics I</b>	<b>3</b>
<b>ECG 731 Theoretical Techniques in Electromagnetics</b>	<b>3</b>
<b>ECG 732 Advanced Engineering Electromagnetics II</b>	<b>3</b>
<b>ECG 733 Plasma I</b>	<b>3</b>

## **Electronics**

<b>ECG 620 Analog Integrated Circuit Design</b>	<b>3</b>
<b>ECG 621 Digital Integrated Circuit Design</b>	<b>3</b>
<b>ECG 720 Advanced Analog IC Design</b>	<b>3</b>
<b>ECG 721 Memory Circuit Design</b>	<b>3</b>
<b>ECG 722 Mixed-Signal Circuit Design</b>	<b>3</b>

## **Power Engineering**

<b>ECG 642 Power Electronics</b>	<b>3</b>
<b>ECG 646 Photovoltaic Devices and Systems</b>	<b>3</b>
<b>ECG 740 Computer Analysis Methods for Power Systems</b>	<b>3</b>
<b>ECG 741 Electric Power Distribution System Engineering</b>	<b>3</b>
<b>ECG 742 Power System Stability and Control</b>	<b>3</b>
<b>ECG 743 Smart Electrical Power Grid</b>	<b>3</b>

## **Signal Processing**

<b>ECG 680 Discrete-Time Signal Processing</b>	<b>3</b>
<b>ECG 682 Introduction to Biomedical Signals and Systems</b>	<b>3</b>
<b>ECG 703 Machine Learning and Applications</b>	<b>3</b>
<b>ECG 760 Random Processes in Engineering Problems</b>	<b>3</b>
<b>ECG 762 Detection and Estimation of Signals in Noise</b>	<b>3</b>
<b>ECG 781 Digital Filters</b>	<b>3</b>
<b>ECG 782 Multidimensional Digital Signal Processing</b>	<b>3</b>
<b>ECG 783 Adaptive Signal Processing with Neural Networks</b>	<b>3</b>

## **Solid State Electronics and Photonics**

<b>[Before] ECG - 614 - Quantum Communication</b>	<b>3</b>
<b>ECG 651 Electronic and Magnetic Materials and Devices</b>	<b>3</b>
<b>ECG 652 Optoelectronics</b>	<b>3</b>
<b>ECG 653 Introduction to Nanotechnology</b>	<b>3</b>
<b>[After] ECG - 712 - Optical Materials and Devices</b>	<b>3</b>
<b>[After] ECG - 715 - Biomedical Photonics</b>	<b>3</b>
<b>[After] ECG - 716 - Space Systems</b>	<b>3</b>
<b>ECG 750 Photonics</b>	<b>3</b>
<b>ECG 752 Physical Electronics</b>	<b>3</b>
<b>ECG 753 Advanced Topics in Semiconductor Devices I</b>	<b>3</b>
<b>ECG 755 Monolithic Integrated Circuit Fabrication</b>	<b>3</b>
<b>ECG 756 Advanced Topics in Semiconductor Devices II</b>	<b>3</b>
<b>ECG 757 Electron Transport Phenomena in Solid State Devices</b>	<b>3</b>
<b>ECG 758 Numerical Methods in Engineering</b>	<b>3</b>

## **Minor Fields Courses – Credits: 9-18**

Select two advisor-approved minor fields and complete coursework (suggested courses below) in each single area totaling 9-18 credits, with a minimum overall average GPA of 3.33. The secondary minor can be from a field outside Electrical Engineering.

## **Communications**

<b>ECG 662 Digital Communication Systems</b>	<b>3</b>
<b>ECG 666 Wireless and Mobile Communication Systems</b>	<b>3</b>
<b>ECG 760 Random Processes in Engineering Problems</b>	<b>3</b>
<b>ECG 762 Detection and Estimation of Signals in Noise</b>	<b>3</b>

## **Computer Engineering**

<b>ECG 600 Computer Communication Networks</b>	<b>3</b>
<b>ECG 603 Embedded Systems Design</b>	<b>3</b>
<b>ECG 604 Modern Processor Architecture</b>	<b>3</b>
<b>ECG 605 Data Compression Systems</b>	<b>3</b>
<b>ECG 607 Biometrics</b>	<b>3</b>
<b>ECG 608 Digital Design Verification and Testing</b>	<b>3</b>
<b>ECG 609 Embedded Digital Signal Processing</b>	<b>3</b>
<b>ECG 617 Internet of Things Systems</b>	<b>3</b>
<b>ECG 700 Advanced Computer System Architecture</b>	<b>3</b>
<b>ECG 701 Reliable Design of Digital Systems</b>	<b>3</b>
<b>ECG 702 Interconnection Networks for Parallel Processing Applications</b>	<b>3</b>
<b>ECG 703 Machine Learning and Applications</b>	<b>3</b>
<b>ECG 704 Coding with Applications in Computers and Communication Media</b>	<b>3</b>
<b>ECG 706 Analysis of Telecommunication and Data Networks</b>	<b>3</b>
<b>ECG 707 Logic Synthesis Engineering</b>	<b>3</b>
<b>ECG 709 Synthesis and Optimization of Digital Systems</b>	<b>3</b>

## **Control Systems Theory**

<b>ECG 770 Linear Systems</b>	<b>3</b>
<b>ECG 771 Optimal and Modern Control</b>	<b>3</b>
<b>ECG 772 Nonlinear Systems</b>	<b>3</b>
<b>ECG 774 Stochastic Control</b>	<b>3</b>
<b>ECG 776 Adaptive Control</b>	<b>3</b>

## **Electromagnetics and Optics**

<b>ECG 630 Transmission Lines</b>	<b>3</b>
<b>ECG 631 Engineering Optics</b>	<b>3</b>
<b>ECG 632 Antenna Engineering</b>	<b>3</b>
<b>ECG 633 Active and Passive Microwave</b>	<b>3</b>



<b>Engineering</b>	
<b>ECG 730 Advanced Engineering Electromagnetics I</b>	<b>3</b>
<b>ECG 731 Theoretical Techniques in Electromagnetics</b>	<b>3</b>
<b>ECG 732 Advanced Engineering Electromagnetics II</b>	<b>3</b>
<b>ECG 733 Plasma I</b>	<b>3</b>

## **Electronics**

<b>ECG 620 Analog Integrated Circuit Design</b>	<b>3</b>
<b>ECG 621 Digital Integrated Circuit Design</b>	<b>3</b>
<b>ECG 720 Advanced Analog IC Design</b>	<b>3</b>
<b>ECG 721 Memory Circuit Design</b>	<b>3</b>
<b>ECG 722 Mixed-Signal Circuit Design</b>	<b>3</b>

## **Power Engineering**

<b>ECG 642 Power Electronics</b>	<b>3</b>
<b>ECG 646 Photovoltaic Devices and Systems</b>	<b>3</b>
<b>ECG 740 Computer Analysis Methods for Power Systems</b>	<b>3</b>
<b>ECG 741 Electric Power Distribution System Engineering</b>	<b>3</b>
<b>ECG 742 Power System Stability and Control</b>	<b>3</b>
<b>ECG 743 Smart Electrical Power Grid</b>	<b>3</b>

## **Signal Processing**

<b>ECG 680 Discrete-Time Signal Processing</b>	<b>3</b>
<b>ECG 682 Introduction to Biomedical Signals and Systems</b>	<b>3</b>
<b>ECG 703 Machine Learning and Applications</b>	<b>3</b>
<b>ECG 760 Random Processes in Engineering Problems</b>	<b>3</b>
<b>ECG 762 Detection and Estimation of Signals in Noise</b>	<b>3</b>
<b>ECG 781 Digital Filters</b>	<b>3</b>
<b>ECG 782 Multidimensional Digital Signal Processing</b>	<b>3</b>
<b>ECG 783 Adaptive Signal Processing with Neural Networks</b>	<b>3</b>

## **Solid State Electronics and Photonics**

[Before] ECG - 614 - Quantum Communication	3
ECG 651 Electronic and Magnetic Materials and Devices	3
ECG 652 Optoelectronics	3
ECG 653 Introduction to Nanotechnology	3
[After] ECG - 712 - Optical Materials and Devices	3
[After] ECG - 715 - Biomedical Photonics	3
[After] ECG - 716 - Space Systems	3
ECG 750 Photonics	3
ECG 752 Physical Electronics	3
ECG 753 Advanced Topics in Semiconductor Devices I	3
ECG 755 Monolithic Integrated Circuit Fabrication	3
ECG 756 Advanced Topics in Semiconductor Devices II	3
ECG 757 Electron Transport Phenomena in Solid State Devices	3
ECG 758 Numerical Methods in Engineering	3

### **Elective Courses – Credits: 9-18**

Complete 9-18 credits of 600- or 700-level MAT, PHY, AST, CEE, CEM, ECG, EGG, CS, ME, or other advisor-approved courses.

### **Dissertation – Credits: 18**

ECG 799 Dissertation

1 – 6

### **Degree Requirements**

All Ph.D. students must satisfy the Ph.D. degree program admission requirements and be admitted to the Ph.D. program on a regular status.

Total credits required depends on the total number of approved graduate-level course work taken as technical electives (with a grade of B or better) during the senior year.

Complete a minimum of 60, 63, or 66 credits (including dissertation credits) respectively corresponding to 9, 6, or 3 credits of formally approved graduate level courses applied toward the B.S. degree yielding a total of 69 course credits. The final division of major, minor, and elective credits will be determined in consultation with the

student's advisor.

Of the 51 required credits, a minimum of 33 credits must be in 700-level courses. Of these 33 credits, a minimum of 30 must be from formal courses. The student's doctoral advisory committee may add more requirements in accordance with the individual's background and field of study.

No more than 6 credits may be from Graduate Independent Study together with Graduate Seminar. No more than 12 credits of a combination of informal courses such as Graduate Independent Study, Special Topics, and Seminar may be applied to the degree program. Complete a minimum of 15 credits in an approved ECE major field, 9 credits an approved ECE minor (primary minor) field, and 9 credits in a second approved open minor (secondary minor) field. Of the 15 credits required in the ECE major field, a minimum of 9 credits must be completed in 700-level courses. A minimum GPA of 3.33 (B+=3.30) must be obtained in the major field. Of the 9 required credits in each minor field, a minimum of 6 credits must be in 700-level courses. A minimum GPA of 3.33 (B+=3.30) must be obtained in each of the minor fields.

Informal courses (Graduate Independent Study, Graduate Seminar, and Special Topics) cannot be applied to the ECE major, ECE minor (primary minor) and the open minor (secondary minor) fields.

All regular (full graduate standing) status graduate students must select a faculty advisor in their first semester.

Students on academic probation may be transferred to the M.S.E. Program depending on the student's academic record. In such a case, the M.S.E. Program requirements must be satisfied. For example, only 6 credits of the informal courses may be applied to the M.S.E. degree program with the further constraint that up to 3 credits total of Independent Study in combination with Graduate Seminar may be in the 6 credits.

Maintain a minimum overall grade point average (GPA) of 3.20, must maintain a minimum GPA of 3.20 each semester, and must complete all graduate level courses that apply towards their degree with a minimum GPA of 2.70 (B-) in each course. Grades below B- cannot be applied towards the Ph.D. degree and must be repeated or replaced. A class grade below C (2.0) is grounds for initiating a program separation recommendation to the Graduate College. Ph.D. candidates who do not maintain an overall minimum GPA of 3.20, who do not maintain a GPA of 3.20 each semester, or who earn more than one grade below B- may either be placed on probation or expelled from the program. The Electrical and Computer Engineering Graduate Committee and/or the Graduate College will determine the terms of the student's probation in accordance with the rules of the Graduate College.

All regular status graduate students must file an approved Plan of Study form before the completion of their third semester. This program must be approved by the student's advisor and the graduate

coordinator. All regular and provisional status graduate students must show satisfactory progress towards completion of their degree by completing at least six credits of their approved program per calendar year. If their progress towards their degree program is not satisfactory, students may either be put on probation or expelled from the program. Before beginning a dissertation, students must have their dissertation topic approved by their advisor, and the necessary paper work including a dissertation prospectus must be filed with the Graduate College by the end of the third semester. The dissertation prospectus describes the dissertation topic and must include an introductory set of sentences, a well formed hypothesis or hypotheses (specifically italicized in the prospectus) accompanied by a motivation, objectives with major and alternative approaches to the studies, and conjectures of possible outcomes. Students are NOT allowed to take dissertation credits until their prospectus is approved. Credits taken before the approval date will NOT count towards the degree program.

## **Graduation Requirements**

[See Plan Graduation Requirements below.](#)

## **Plan Graduation Requirements**

During the first semester, a Ph.D. student must select a faculty advisor. The faculty advisor does not have to be the one to whom the student was assigned upon entering the Ph.D. program. In coordination with the faculty advisor, the student must also form a doctoral advisory committee. A doctoral advisory committee is composed of at least four members of the UNLV Graduate Faculty. Three of the faculty must be from the Department of Electrical and Computer Engineering. The fourth from a relevant supporting field having Full Graduate Faculty Status as recognized by the Graduate College.

Students admitted on provisional and/or conditional status are not allowed to take the qualifying exam until their provisions and/or conditions have been met. Students taking the exam while on provisional or conditional status will be required to retake the exam regardless if one or all areas of the exam have been passed.

Provisional status students must complete all required supplementary work within one calendar year from the time of admission into the program with a grade of B (3.0) or better in each course.

Pass the Qualifying Exam within 2 semesters of being admitted to the Ph.D. program on a regular (full graduate standing) status. The Qualifying Exam is offered once every fall semester and once every spring semester. This exam cannot be taken more than twice.

The Qualifying Exam tests the student's general undergraduate knowledge of electrical engineering and computer engineering. To register for the Qualifying Exam, eligible students must notify the graduate coordinator no later than one month prior to the examination date.

All students must pass the Qualifying Exam within the first two semesters (excluding the summer semester) upon being admitted to the Ph.D. program on a regular status. If a student is required to take the qualifying exam and is not present to sit the exam, an automatic FAIL is assigned. Students who have not passed the Qualifying Exam within this time frame will be terminated from the Ph.D. program. Students who have not passed the Qualifying Exam by their second attempt will be terminated from the Ph.D. program. Students in the Direct Ph.D. program who fail the Qualifying Exam on their second attempt within the two semester time frame may elect to pursue a M.S. Degree by completing all of the requirements listed for that degree.

The Qualifying Exam is a four and one-half hour exam covering questions in the following undergraduate electrical and computer engineering fields:

Communications

Control System Theory

Electromagnetics and Optics

Electronics

Power

Signal Processing

Solid State

Digital Logic Design

Computer Architectures

Embedded Systems

Computer Communication Networks

To pass the qualifying exam requirement, the student must successfully complete any four of the eleven areas with a grade of PASS to complete the qualifying exam requirement within two sittings. If the student passes less than four areas on the first attempt, the student will receive a PASS for those individual areas successfully completed and will not be required to retake these areas on the second attempt. The exam is a closed note, closed book exam.

For more details on course specifics, exam logistics, appeal rights and procedure, and protocols regarding the qualifying exam, refer to the ECE department's Electrical Engineering Graduate Program Document.

In all Post-Bachelor's subplans, a Ph.D. student must complete a minimum of 15 credits in an approved ECE major field in a single area of Electrical and Computer Engineering, 9 credits in an approved ECE minor field (primary minor) in a single

but different area of Electrical and Computer Engineering, and another 9 credits in a second approved minor (secondary minor) field. Currently, the Department of

Electrical and Computer Engineering at UNLV offers Communications, Computer Engineering, Control System Theory, Electromagnetics and Optics, Electronics, Power Systems, Signal Processing, and Solid State Electronics and Photonics as major fields. Specific courses that can be applied to specific fields are listed in detail in the Electrical Engineering Graduate Program Document.

Of the 15 credits required in the ECE major field, a minimum of 9 credits must be completed in 700-level courses. To complete the ECE major field requirement, the applied 15 credits of ECE major course work must attain a minimum overall GPA of 3.33 (B+=3.30).

Each student must complete two minor fields. To complete a minor field, a student must complete a minimum of 9 credits in a minor field and have an overall minimum GPA of 3.33 (B+=3.30) for the 9 minor field credits. Of the 9 required credits in each minor field, a minimum of 6 credits must be in 700-level courses. Courses that can be applied to specific minor fields are listed in detail in the Electrical Engineering Graduate Program Document. These courses may be applied to any designated field but may only be counted once. With the written approval of the major advisor and the student's advisory committee, the secondary minor may be a mixed minor field. A mixed minor field may be formed with courses inside and/or outside of the Electrical Engineering Department's approved fields (e.g., mathematics and physics, computer engineering and computer science, physics, mechanical engineering, solid state and electromagnetics) A mixed minor may not be composed of courses in the Electrical Engineering Department that satisfy course work in the major and the other minor field. The only exception is when a course may be used in more than one field. In this case, the course may not be counted twice but may be used for either minor area. However, the student must complete at least one minor field (primary minor field) in Electrical Engineering in a single area.

After successfully completing all required course work, the candidate must pass the Preliminary Exam. The Preliminary Exam cannot be taken more than once per semester but may be repeated until passed.

The Preliminary Exam evaluates the caliber of a student's dissertation topic. The Preliminary Exam cannot be taken more than once per semester but may be repeated until passed.

To be eligible for the Preliminary Exam, a student must have successfully completed all required course work except for the 18 credits of Dissertation.

Before the Preliminary Exam, a student must prepare a 10 to 20-page prospectus of their research. A copy of this prospectus must be submitted to the Graduate Committee and each member of the Ph.D. candidate's advisory committee at least two weeks prior to the Preliminary Exam.

The student must also notify the Graduate Committee and each member of their advisory committee of the date, time and location of their Preliminary Exam. This must be done at least two weeks prior to the Preliminary Exam.

During the Preliminary Exam, the student presents their prospectus to their advisory committee. To pass the Preliminary Exam, the student's advisory

committee must unanimously approve the student's prospectus. Students who pass the Preliminary Exam are advanced to candidacy for the Ph.D.

Complete a minimum of 18 credits of Dissertation and complete a dissertation containing original research. Upon completion, the student must pass the Final Exam in which the student defends their dissertation. The Final Exam is the culminating experience of the PhD program.

The Final Exam evaluates the Ph.D. candidate's dissertation. The Final Exam cannot be taken more than once per every three months but may be repeated until passed. To be eligible for the Final Exam, a Ph.D. candidate must have passed the Preliminary Exam, and have successfully completed all required course work including a minimum of 18 credits of Dissertation. A minimum of 12 credits of Dissertation must be taken after the successful completion of the Preliminary Exam. A copy of the Ph.D. candidate's dissertation must be submitted to the Graduate Committee and each member of the Ph.D. candidate's advisory committee at least two weeks prior to the Final Exam. The Ph.D. candidate must also notify the Graduate Committee and each member of their advisory committee of the date, time, and location of their Final Exam at least two weeks prior to the Final Exam. During the Final Exam, the Ph.D. candidate will present their dissertation to their advisory committee. To pass the Final Exam, the Ph.D. candidate's advisory committee must unanimously approve the Ph.D. candidate's dissertation.

The Department of Electrical and Computer Engineering requires that the Ph.D. degree be completed within a period of six years from the time the candidate is fully admitted to the Ph.D. program. Further, courses taken more than six years prior to graduation cannot be applied toward the PhD degree without permission from the Graduate College. Students exceeding this time limit must formally write a letter requesting permission from both the Graduate Committee and the Graduate College to stay in the Ph.D. program and apply coursework towards the degree program. The formal letter must explain the circumstances of why the degree was not completed within the allotted time frame and indicate the extended period of time needed to complete the degree.

The student must submit all required forms to the Graduate College and then apply for graduation up to two semesters prior to completing their degree requirements.

The student must submit and successfully defend their dissertation by the posted deadline. The defense must be advertised and is open to the public.

After the dissertation defense, the student must electronically submit a properly formatted pdf copy of their dissertation to the Graduate College for format check. Once the dissertation format has been approved by the Graduate College, the student will submit the approved electronic version to ProQuest. Deadlines for dissertation defenses, format check submissions, and the final ProQuest submission can be found [here](#).

## Advising and Culminating Experience

With these changes,  Yes  
will students in this  
program need a Grad  
Advisory Committee  
(GAC) formed?  No

If yes, please list the  
applicable subplans  
that will need a GAC:

With these changes,  Yes  
will students be  
assigned a faculty  
advisory prior to GAC  
formation?  No

If yes, please list the  
applicable subplans  
that will need an  
advisor:

Comments:

For Master's program  
only: With these  Yes  
changes, does this  No  
program require a  
prospectus form?

If yes, please list  
applicable subplans:

With these changes,  Yes  
will the culminating  
experience be a  No  
course?

If yes, please provide  
course prefix, number  
and name:

With these changes,  
describe the  
culminating  
experience  
(requirements, if  
applicable-which  
subplans will need  
GACs, forms, etc.):

The [Degrees Directory](#) provides current and consistent degree information. Submission of this form indicates acknowledgment and understanding that every department is responsible creating and maintaining accurate and updated program information on the UNLV Degrees Directory.

If new courses are added as placeholders within this proposal, new courses must be created using a Course Create form simultaneously to the process of this proposal.



**Degrees Directory Program Entry:**  Check this box to acknowledge the above statement.

**Changes will be applicable to:**  Current Students  
 New Students  
 Both Current and New Students



**If applicable to current students, changes are:**  Mandatory  Optional


**If mandatory:**  If mandatory, I confirm that all students will be notified in writing of these changes as approved by the graduate college  
 Not Mandatory

**Effective Date:**

## READY TO SUBMIT?

After making all your intended changes, please follow these steps:

1. Finish the launch of your proposal by clicking the decisions icon  located to the right of the form. This will display a new decision/approval field on the top right.
2. Click on "approve", add an optional comment if necessary, and then click on the "Make My Decision" button at the bottom to move the proposal forward to the next step. You will see a notification indicating that the proposal has moved forward. You will not be able to edit after moving the proposal forward.
3. Please note that it is your responsibility as the proposer to see that the proposal is reviewed and receives all necessary approvals. Please be encouraged to reach out to reviewers on each step, if necessary.
4. You can check the status of the proposal by clicking on the workflow status icon  to verify that the proposal has gone to the next step.

The workflow status icon  will also show you the current step of the proposal at any given time, and who are the reviewers at that step.


QUESTIONS? contact [gradcurriculum@unlv.edu](mailto:gradcurriculum@unlv.edu)


### III. Department Vote Information

Note: This section is to be filled out by the Department Chair on behalf of the department committee.

(The role has been assigned to the corresponding person on this step. If incorrect, please notify [gradcurriculum@unlv.edu](mailto:gradcurriculum@unlv.edu)).

1. Review the proposal. Discuss and make appropriate revisions.
2. Fill in vote information in the fields below, along with the approval. If Dual or Interdisciplinary: add votes from all departments/colleges involved

3. Then approve/reject by clicking the decisions icon  located to the right of the form. This will display a new decision/approval field on the top right. Click on "approve", add an optional comment if necessary, and then click on the "Make My Decision" button at the bottom to move the proposal forward to the next step. You will see a notification indicating that the proposal has moved forward. You will not be able to edit after moving the proposal forward.

The workflow status icon  will also show you the current step of the proposal at any given time, and who are the reviewers at that step.

If there were any modifications to the proposal, please enter them in the comments box below:

**Comments:**

**Date faculty voted on proposal:** 3/3/2022

**Result of vote:** 12-0

**Manner of vote:** online


## IV. College Vote Information

Note: This section is to be filled out by the Academic Associate Dean on behalf of the college/school committee.

(The role has been assigned to the corresponding person on this step. If incorrect, please notify [gradcurriculum@unlv.edu](mailto:gradcurriculum@unlv.edu)).

1. Review the proposal. Discuss and make appropriate revisions.
2. Fill in vote information in the fields below, along with the approval. If Dual or Interdisciplinary: add votes from all departments/colleges involved

3. Then approve/reject by clicking the decisions icon  located to the right of the form. This will display a new decision/approval field on the top right. Click on "approve", add an optional comment if necessary, and then click on the "Make My Decision" button at the bottom to move the proposal forward to the next step. You will see a notification indicating that the proposal has moved forward. You will not be able to edit after moving the proposal forward.

The workflow status icon  will also show you the current step of the proposal at any given time, and who are the reviewers at that step.

**Date faculty voted on proposal:** 03/25/2022

**Result of vote:** 4/0/0

**Manner of vote:** online

## V. Graduate College Use Only - Code Request Items

**Note: This section is for graduate college use only.**

**Academic Organization:**

**Academic Program Code:**

**Academic Plan Code:**

**Plan Description (30 chars):**

**Transcript Description:**

**Subplan Code:**

**Subplan Description:**

**Subplan Type:**  Track  
 Concentration  
 Option/Dual  
 Embedded/Track

**Subplan on Transcript?**  YES  NO

**Effective Date and First Term Valid:**

**Length / Avg. Time to Degree:**

**CIP code and title:**

**Other Codes Needed:**

**Description /  
implementation field  
(do not edit)**

## Comments for Doctor of Philosophy - Electrical Engineering

<b>Curriculog</b>	3/28/2022 10:04 am <a href="#">Reply</a>
Mohamed Trabia has approved this proposal on School/College Associate Dean/ Dean.	
<b>Melissa Morris</b>	3/25/2022 3:50 pm <a href="#">Reply</a>
approved on 3/25	
<b>Curriculog</b>	3/25/2022 3:50 pm <a href="#">Reply</a>
Melissa Morris has approved this proposal on School/College Committee.	
<b>Curriculog</b>	3/17/2022 3:06 pm <a href="#">Reply</a>
ECE Chair has approved this proposal on Department Chair.	
<b>Curriculog</b>	3/11/2022 4:33 pm <a href="#">Reply</a>
ECG Graduate Coordinator has approved this proposal on Graduate Coordinator.	
<b>Curriculog</b>	3/11/2022 2:19 pm <a href="#">Reply</a>
Graduate Curriculum has approved this proposal on Technical Review.	
<b>Curriculog</b>	2/18/2022 1:19 pm <a href="#">Reply</a>
Mei Yang has approved this proposal on Originator.	
<b>Graduate Curriculum</b>	2/11/2022 3:33 pm <a href="#">Reply</a>
Moving back to originator for review	
<b>Curriculog</b>	2/11/2022 3:33 pm <a href="#">1 Reply</a>   <a href="#">Reply</a>
Graduate Curriculum has rejected this proposal on Technical Review.	
<b>Mei Yang</b>	2/11/2022 4:17 pm
Can you let me know what issues exist on the proposal? So we can revise and resubmit the program change. Thanks.	
<b>Curriculog</b>	2/9/2022 11:33 am <a href="#">Reply</a>
Mei Yana has approved this proposal on Originator.	

Mei Yang has approved this proposal on 9/29/2021 10:13 am

**Curriculog**

9/29/2021 10:13 am [Reply](#)

Mei Yang has launched this proposal.

**Curriculog**

9/29/2021 10:05 am [Reply](#)

Mei Yang imported from the map 2022-2023 - Working Graduate Catalog into the following proposal fields: I. General Information: Department (s) (if Dual or Interdisciplinary please add all departments);, I. General Information: Degree or Certificate Name:, I. General Information: Degree Type:, I. General Information: Program Type:, II. Program Changes: Proposed Curriculum:, V. Graduate College Use Only - Code Request Items: Description / implementation field (do not edit).